M.G. M.'s COLLEGE OF ENGINEERING, NANDED

Department of Electronics and Telecommunication Engineering

Electronics Technocrates Association (ETA)

Name of the activity:	**Career Counseling session: Opportunities in VLSI Design Mr. Rushikesh Bongane, DFT Engineer, Ampere Computing Pvt. Ltd. , Banglore**
Date:	12/04/2025
Activity Coordinator:	Mr. D. J. Tuptewar, Ms. Maithili Rao, Dr. M. R. Banwaskar
Number of participants:	66

Introduction:

The Department of Electronics and Telecommunication Engineering organized an expert lecture on "Career Opportunities in VLSI" delivered by our distinguished alumnus. The session aimed to bridge the gap between academic learning and industry requirements in the domain of Very-Large-Scale Integration (VLSI). With the semiconductor industry in India gaining momentum, this lecture provided valuable insights into the skills, roles, and roadmap for students aspiring to build a career in VLSI design and development.

Objective:

- To make students aware of the current trends and future scope in the VLSI industry.
- To provide guidance on essential technical skills and tools used in VLSI such as Verilog, SystemVerilog, VHDL, FPGA, ASIC design, etc.
- To explain various job roles and career paths in core electronics, specifically in chip design and testing.
- To motivate students by showcasing the career journey of a successful alumnus in the VLSI domain.

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Outcome:

- Students understood the core concepts and skills required to enter the VLSI field.
- Gained clarity on various job roles such as Design Engineer, Verification Engineer, and Physical Design Engineer.
- Students became familiar with the importance of internships, project-based learning, and domain-specific certifications.
- Enhanced interest among students to pursue specialized courses and higher studies related to VLSI and semiconductor technologies.

Program Outcomes (POs) Addressed:

- **PO1:** Engineering knowledge – Applied fundamentals of electronics in understanding VLSI design flow.
- **PO2:** Problem analysis – Exposure to real-world chip design problems and solutions.
- **PO3:** Design/development of solutions – Insight into VLSI design techniques and methodologies.
- **PO5:** Modern tool usage – Awareness about industry-standard EDA tools and simulation platforms.
- **PO12:** Life-long learning – Motivation to pursue continuous learning in rapidly evolving VLSI technologies.

Feedback:

Students found the session highly informative and inspiring. They appreciated learning from an alumnus who shared real-life industry experiences, challenges, and tips to succeed in the core electronics field. The session helped many clarify their career goals and understand the importance of skill-building during their academic years. Students requested more such alumni interactions and hands-on workshops on VLSI tools.

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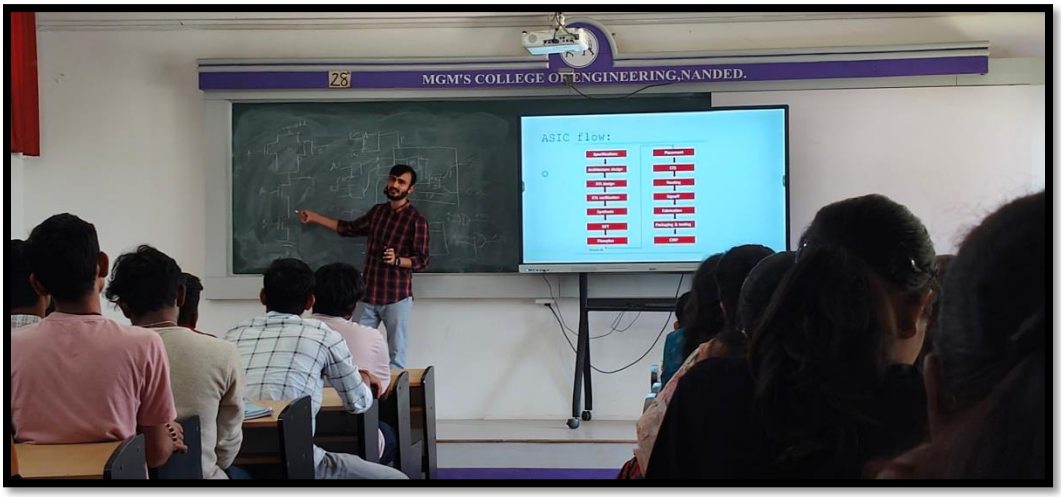
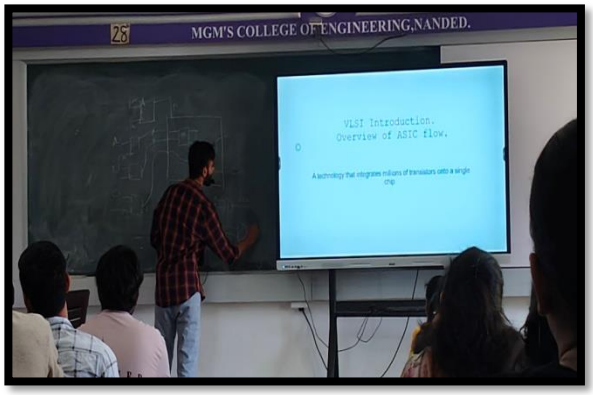
Glimpses of Career Counseling session by Mr. Rushikesh Bongane





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