



**M.G. M.'s COLLEGE OF ENGINEERING, NANDED**  
**Department of Electronics and Telecommunication Engineering**



Electronics Technocrats Association (ETA)

Name of the activity:	Internship Opportunity Guidance
Date:	07/04/2025
Resource Person	Dr. Y. V. Joshi and Mr. Vishwas Kurundkar
Activity Coordinator:	Ms. Maithili Rao, Dr. M. R. Banwaskar
Number of participants:	B.Tech Final Year

**Introduction:**

The Electronics Technocrats Association (ETA) organized an Internship Opportunity Guidance Session for Final Year B.Tech students of the Electronics and Telecommunication Engineering Department. session focused on creating awareness about internship opportunities in the domain of **Embedded Systems** at Vidya Lab, Pune. Students were guided about the importance of industry-oriented training, practical exposure, and hands-on experience in core technical domains. As a result of this guidance and interaction, 12 final-year students successfully secured internship opportunities in Embedded Systems at Vidya Lab, Pune. The session bridged the gap between academic learning and industry requirements.

**Objective:**

- To create awareness among students about internship opportunities in the Embedded Systems domain.
- To guide students about the skills required for Embedded Systems industries.
- To motivate students to apply for core technical internships.
- To enhance students' practical knowledge beyond academic curriculum.
- To connect students with industry exposure and real-time project experience.
- To prepare students for placement opportunities in core companies.

**Outcome:**

- 12 Final Year B.Tech students secured internship opportunities at Vidya Lab, Pune in the Embedded Systems domain.
- Students gained clarity about industry expectations and required technical skills.
- Improved confidence among students to pursue core technical careers.
- Enhanced awareness about real-time project development and industry practices.
- Strengthened industry–institute interaction for future collaboration.



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#### Program Outcomes (POs) Addressed:

**PO2: Problem Analysis** – Students understood industry-level technical problems and practical implementation challenges in Embedded Systems.

**PO3: Design/Development of Solutions** – Exposure to real-time embedded projects enhanced their design and development skills.

**PO5: Modern Tool Usage** – Students gained awareness about modern embedded tools, microcontrollers, and development platforms used in industry.

**PO12: Life-long Learning** – Encouraged continuous learning and upskilling in emerging embedded technologies.

#### Feedback:

- Students found the guidance session highly informative and motivating.
- Selected students expressed excitement about gaining hands-on industrial experience.
- Many students requested more such internship-oriented sessions in core domains.
- Faculty appreciated the positive outcome of 12 successful selections.

#### Conclusion:

The Internship Opportunity Guidance Session proved to be highly beneficial for the Final Year students. The selection of 12 students at Vidya Lab, Pune reflects the effectiveness of timely guidance and industry interaction initiatives conducted by the department.

Dr. Mangal Banwaskar  
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ETA Coordinator

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