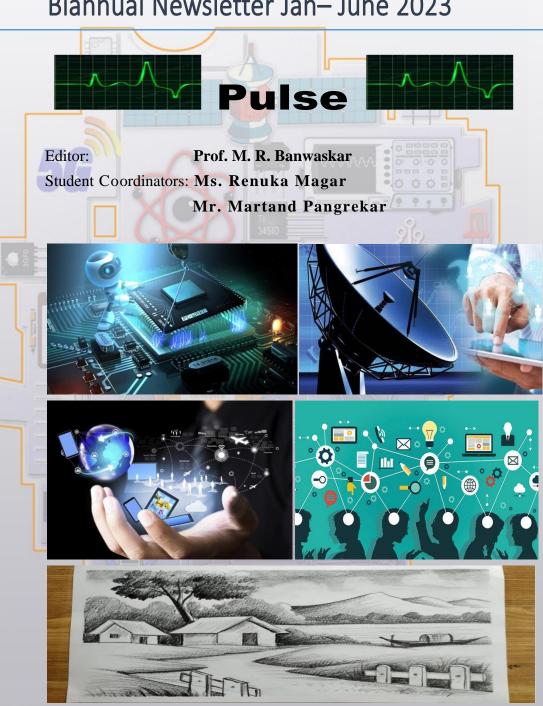




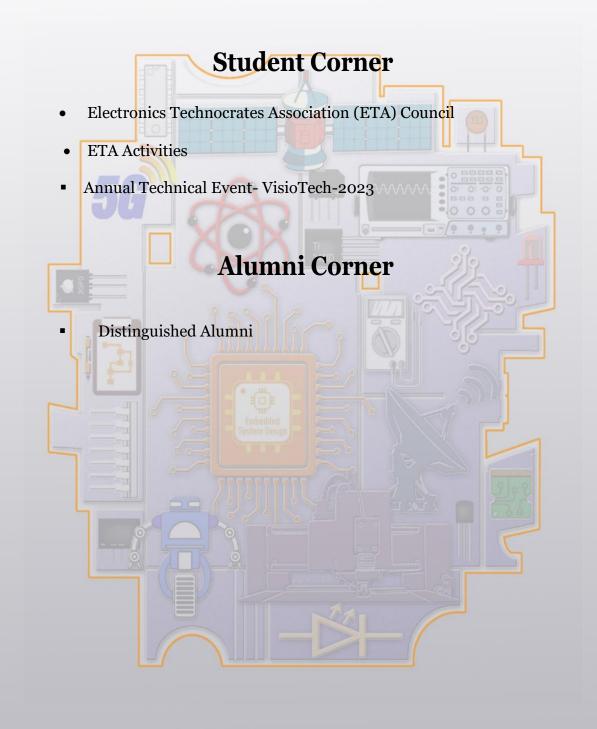
MGM's College of Engineering, Nanded Department of Electronics and Telecommunication Engineering

Biannual Newsletter Jan-June 2023





Inside this issue





MGM's College of Engineering, Nanded

Department of Electronics and Telecommunication Engineering

Vision

To be one of the leading departments for Electronics and Telecommunication Engineering to develop proficient Electronics Engineers with global acceptance in the service of mankind

Mission

M1: Promoting academic excellence in Electronics and Telecommunication engineers through experiential learning and industry engagement, focusing on Communication Engineering, VLSI, Embedded Systems and Internet of Things (IoT) for global competitiveness.

M2: Inculcating professionalism, innovation, teamwork, and ethical values in 5G Technology, Artificial Intelligence & Machine Learning, Green and Sustainable Technology, and Biomedical Electronics for the benefit of Society and Industry.

M3: Elevating student employability through hands-on programming education and cultivating a robust collaborative network with industry, academic experts, alumni, and student projects.

M4: Empowering faculty with continuous training in AIML, Medical Image Processing, Signal Processing, and Pedagogy to improve student learning and research productivity.



Program Educational Objectives (PEOs):

Graduates of Electronics and Telecommunication employed will:

PEO 1: Examine semiconductor device physics and enhance skills in advanced modeling and simulation using computational methods.

PEO 2: Master signal and Image processing principles as a core skill and specialize in designing embedded and VLSI systems.

PEO 3: Keep up with cutting-edge research in Signal and Image Processing, Communication systems and IoT.

PEO 4: Foster an academic environment that emphasizes excellence, leadership, and ethical standards in students, equipping them with lifelong learning skills and essential research components for a successful professional career.

Program Specific Outcomes (PSOs):

Graduates will be able to:

PSO 1: Employ modern tools to model, simulate, experiment with, and analyze the performance of Electronics and Telecommunication systems.

PSO2: Address economic, social, environmental, ethical, health and safety issues keeping in pace with latest technological concepts.

PSO3: Drive need-based innovations in Electronics Telecommunication Engineering, and fostering 'Make in India' through an understanding of finance management and entrepreneurship product development.

STUDENT CORNER

Electronics Technocrates Association (ETA)

Beyond classroom, laboratory and academic experience, our student club ETA is a platform for holistic development of a students with opportunities to explore interests, groom hidden talents, meet and network with other students, interact with experts and to grow technically, professionally and personally.

Objectives of ETA:

To organize:

- Expert Lectures by Academicians, Alumni, industry experts
- Project Competitions, Debate competitions, Quiz
- Poster presentation, sketches, painting competitions
- Workshops, seminars, webinars
- Career guidance sessions
- Social activities, Campus Connect activities

In order to plan and execute various activities, ETA Council is formed in each academic year. It consists of Vice-president, Secretary, Treasurer, coordinator, Event Head and board members. Depending on student interest and capability, the selection of ETA council members is done.

ETA Council

No.	Name	Class	Position	
1	Renuka Magar	Final year	Vice president	
2	Martand Pangrekar	Final year	Secretary	
3	Shrutika Gundawar	TY	Treasurer	
4	Purushottam Sing	Final year	Coordinator	
5	Poornima Bamne	Final year	Technical event Head	
6	<mark>Shrey</mark> a Zarkar	TY	Board member	
7	Sanskar Kotalwar	TY	Board member	
8	Shweta Suryawanshi	TY	Project Activity Head	
9	Arti Boga	345ID TY	Aptitude T <mark>rainin</mark> g Head	
10	Shweta Sahare	TY	Wall Magazine Head	
11	Rudrani Dachawar	TY	Board member	
12	Nagesh Kharat	SY	Board member	
13	Sanskruti More	SY	Board member	
14	Vaishnav S <mark>asan</mark> e	SY	Board member	
15	Avinash Ra <mark>tho</mark> d	SY	Board member	

Ms. Mangal R. Banwaskar ETA- Coordinator

Dr. Kalpana P. Paithane ETA - President

ETA Council



Renuka Magar Vice-president



Martand Pangrekar Secretary



Poornima Bamne Technical Event Head



Shrutika Gundawar Treasurer



Shweta Suryawanshi Project Activity Head



Arti Boga
Aptitude Training Head



Avinash Rath<mark>od</mark> Board Member



Nagesh Kharat Board Member



Sanskruti More Board Member



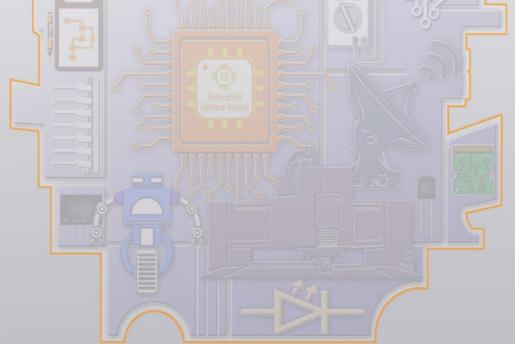
Rudrani Dachawar Board Member



Shambhavi Chandajkar Board Member

ETA Activities

No.	Activity	Date	Participants
1	Project Based Learning	4 Jan 2023	SY students
2	Campus Connect: Mini Project Demo at Madhyamic Vidyalaya Babhali		SY students
3	Campus Connect: SY students visit to Priyadarshini Vidya Sankul, Nanded for Miniproject Demo	12 Jan 2023	SY students
4	Technical Talk: Topic: Current Technology Trends and Career in VLSI Recourse Person: Mr. Sainath Kurude, Engineering Manager, Intel Technologies Pvt. Ltd., Bangluru	23 Jan 2023	All students
5	Book Reading Activity	27 Mar. 2023	SY students



Project Based Learning (PBL)

PBL allows students to apply theoretical knowledge to real-world engineering problems. This hands-on approach enhances their understanding of concepts and fosters critical thinking skills. It also encourages collaboration among students, promoting effective communication, project management and teamwork skills. Problem-solving Skills: Engineering projects typically involve complex challenges that require innovative solutions. PBL helps students develop problem-solving skills by tackling real problems with practical constraints.

Students developed projects on ideas like insect repellant, water tank cleaning device, Laser based farm fencing etc.





PBL Activity



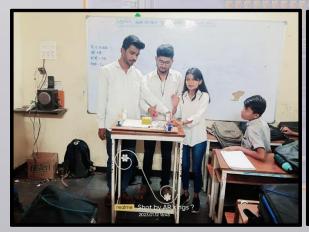
Campus Connect: Mini Project Demo at Schools

Engineering students showcasing their projects in schools serve as inspiring role models for younger students, encouraging them to engage in hands-on learning experiences. These demonstrations ignite curiosity in students, motivating school them to explore engineering concepts in a practical manner. By sharing their projects, engineering students not only demonstrate the applicability of classroom knowledge but also inspire the next generation of innovators and problem-solvers. This interaction fosters a collaborative learning environment, bridging the gap between academia and real-world applications.

Ultimately, such initiatives nurture a culture of creativity and experimentation, empowering students to pursue their interests in engineering and technology.









Mini-project demo at Madhyamic Vidyalaya, Babhali Village



Technical Talk: Topic: Current Technology Trends and Career in VLSI

The talk was arranged on 23rd Jan 2023 to provide insights into the career prospects and opportunities in the field of VLSI.

Recourse Person: Mr. Sainath Kurude, Engineering Manager, Intel

Technologies Pvt. Ltd., Bangluru

The diverse roles available in VLSI engineering, ranging from chip design and verification to system-level integration, and the skills and qualifications required to excel in these roles was discussed. By showcasing successful professionals and their contributions to the field, the talk aimed to inspire the students to pursue careers in VLSI. It emphasized the importance of innovation, continuous learning, and perseverance in navigating the dynamic landscape of VLSI technology.

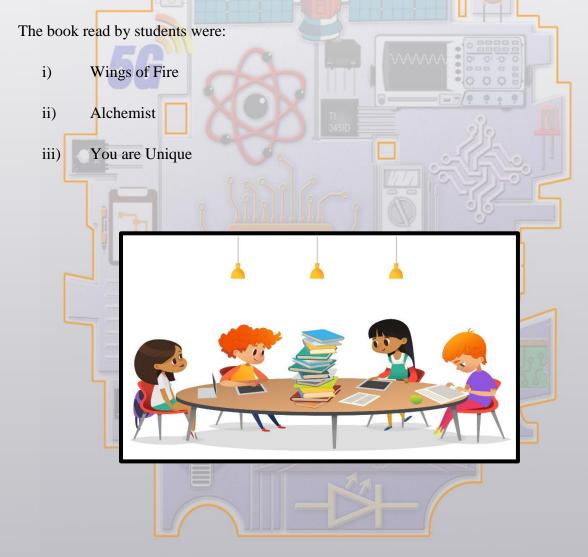


Mr. Sainath Kurude delivering Technical Talk



Book reading activity

It offers a valuable opportunity for individuals to engage with literature, broaden their horizons, and enhance their cognitive skills. Participants delve into diverse genres, exploring different perspectives and cultures through the pages of a book. This activity fosters a love for reading, encourages critical thinking, and promotes lifelong learning habits. Whether enjoyed individually or as part of a group, book reading activities spark intellectual curiosity and creativity. Ultimately, they serve as a gateway to knowledge, empathy, and personal growth.



VISIOTECH- 2023

VISIOTECH is a State level Event which is conducted every year to provide platform for budding engineers to exhibit their technical skills in various events and competitions. The name VISIOTECH was coined as it represents Vision of Technocrats. VISIOTECH is packed with multiple events testing knowledge and capabilities of would be engineers. The event dates: 11 and 12 May 2023.









Inauguration of the event was done at the hands of Dr. M. B. Kokre, Director, SGGSIE&T, Nanded in presence of Hon. Chairman Kamalkishor Kadam and Director Dr. Geeta S. Lathkar.

ELECTRONICS AND TELECOMMUNICATION ENGINEERING EVENTS

1. MINUTE TO WIN IT:

STUDENTS WILL BE GIVEN SOME TECHNICAL TASK. THEY HAVE TO COMPLETEIT WITHIN A MINUTE.

2. CIRCUIT DESIGNING:

YOU WILL BE GIVEN ELECTRONIC COMPONENTS AND CIRCUIT DIAGRAM AND YOU WILL HAVE TO DESIGN THE CIRCUIT IN GIVEN TIME

3. CIRCUIT DEBUGGING:

DO YOU BELIEVE YOU ARE AN EXPERTISE IN DEBUGGING ANY CIRCUIT QUICKLY? JOIN THE EVENT AND SHOW YOUR TALENT IN DEBUGGING.

4. CRYPTIC CROSSWORD PUZZLE:

STUDENTS WILL BE ASKED SOME TECHNICAL QUESTIONS AND THEY WILL HAVE TO ANSWER THEM IN GIVEN TIME







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Visiotech- 2023 Activities

Faculty Coordinator: Mr. Kude V.P.

Student Coordinators: Sanskar Kotalwar B. Tech. Final

Shrutika Gundawar B. Tech. Final

S.No.	Event	Details
1	MINUTE TO WIN IT	Students were given some technical task to be completed in one minute.
2	CIRCUITRIX	To test the designing skills of participants, they were given circuit design probelms
3	CIRCUIT DEBUGGING	To test the analytical ability of participants, they were asked to find bug in the circuit.
4	TECHNICAL RANGOLI	Participants exhibited the blend of technical skill and art through this event







Visit to Dept. by Hon. Chairman Sir, Director Madam and Dr. M. B. Kokre, Director SGGIE&T, Nanded during Visiotech -2023





Glimpses of the various VisioTech-2023 events at Dept. of ECT



Alumni Corner

Alumni are the real ambassadors of any institute. Their contribution in development of institute and feedback really helps in continuous improvement in teaching-learning as well as placement activities. MGMCOE ECT alumni are holding high rank positions in industry and academia in India and abroad.



I am deeply grateful to the Department of Electronics and Telecommunication Engineering at MGMCOE, Nanded for the solid foundation and continuous support that have been pivotal to my success. The knowledge and skills I acquired during my time there have been indispensable in my journey, and I am confident that the department will continue to produce outstanding engineers who will make significant contributions to the field.

Thank you once again for all that you do. I look forward to maintaining a strong connection with the department and contributing in any way I can to its ongoing success.



Vishal Shukla
Senior Project Manager, Tata Technologies, Pune

The comprehensive curriculum, dedicated faculty, and state-of-the-art facilities provided by the department were instrumental in shaping my career. The emphasis on both theoretical knowledge and practical application prepared me to tackle real-world challenges with confidence and competence. Courses that covered advanced topics in electronics, telecommunications, signal processing, and embedded systems were particularly beneficial, giving me a competitive edge in the industry.



Gaurav Shrivastava CEO/CTO, Xlligent Softwars, Pvt, Ltd, Pune

As an alumnus of the Department of Electronics and Telecommunication Engineering at MGMCOE, Nanded, I express my sincere gratitude for the exceptional education and experiences I received during my time there. Currently, I am privileged to hold the position of CEO at Xlligent Softwars, Pvt, Ltd, Pune and I owe much of my success to the solid foundation laid by our esteemed department.





